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OPTIMAL ASSIGNMENT OF COMPUTER ORAGE BY CHAIN DECOMPOSITION OF PARTIALLY ORDERED SETS

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George B. Dantzig and Gary H. Reynolds

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OPTIMAL ASSIGNMENT OF COMPUTER STORAGE BY CHAIN DECOMPOSITION OF PARTIALLY ORDERED SETS

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George B. Dantzig and Gary H. Reynolds Operations Research Center University of California, Berkeley

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ABSTRACT

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To save storage, a program is usually written so that each variable assumes several values. As a result, a program is usually difficult to understand and prone to errors. For an important class of programs, it will be shown that they can be written with complete freedom in the naming of variables; leaving the task of minimizing storage requirements to the computer itself.

Optimal Assignment of Computer Storage by Chain Decomposition of Partially Ordered Sets

by

George B. Dantzig and Gary H. Reynolds

THE PROBLEM: Given a sequence of n steps, on the k-th step a value ν_k is computed as a function F_k of the previously computed values $\nu_1,\,\nu_2,\ldots,\,\nu_{k-1}$. In general, only a subset of these is required to compute ν_k . We are interested in finding the minimal number of locations in the memory of the computer to store the values ν_i so that they will be available for computing the successive functions F_k . A saving in the number of locations occurs whenever a value ν_k is stored in the same location as a previously computed ν_i , which is no longer needed for step k+1, k+2,..., n .

MOTIVATION: Consider the following trivial example:

Step		Inputs
1.	ν _į = given	ν ₁
2.	ν ₂ = given	v ₃
3.	$v_3 = v_1 + v_2$	ν ₄
4.	$v_4 = v_2 + v_3$	V ₅
5.	$v_5 = v_3 + v_4$	Output
6. 0	utput = ν _ς	

The arrows in the figure indicate the node values needed to compute ν_1 , ν_2 ,.... Thus ν_3 requires ν_1 and ν_2 , etc. The logical dependence is, accordingly,

$$v_1 = F_1(constant given)$$

$$v_2 = F_2(constant given)$$

$$v_3 = F_3(v_1, v_2)$$

$$v_4 = F_4(v_2, v_3)$$

$$v_5 = F_5(v_3, v_4)$$
Output = $F_6(v_5)$

A programmer who is a <u>memory miser</u> would notice the dependence of one step on another and would store v_3 in the same location as v_1 (since v_1 is not needed after step 3), v_4 in the same location as v_2 , and v_5 in the same location as v_3 . Only two locations are needed which he calls, say, "x" and "y". He accordingly writes the following program:

Step

- 1. x = given input
- 2. y = given input
- 3. x = x+y
- $4. \quad y = y + x$
- $5. \quad x = x+y$
- 6. output x

or some such nonsense, which we will refer to as "memory misering algebra."

This multiple use of the same symbol is a recognized cause of program error. It is one of the primary reasons why one programmer has the greatest difficulty in understanding a program written by another (or even one by himself). To avoid multiple use of the same symbol for memory misering, a programmer can make use of special instructions which will direct the machine language compiler to store the values of different symbols in the same location. This is of some help, but leaves the task of conserving storage location up to the programmer and again is subject to

error.

Our thesis is that memory misering is essentially clerical in nature, a task unworthy of the programmer's time. We will show for one important class of programs that the task of conservation of memory location can be done efficiently by the machine as part of its translation of a program into machine language.

SOLUTION: Define for each v_k an interval of storage. If v_k is <u>last</u> needed to compute F_ℓ , then its <u>interval</u> of time for storage is from step k+1 to $\ell_k = \ell$ and is denoted by

$$I(v_k) = [k+1, \ell_k] .$$

We define an interval $I(v_k)$ as coming <u>before</u> another interval $I(v_{k'})$ when $\ell_k < k'+1$, which we write as

$$I(v_i) < I(v_{k'})$$
 if $\ell_k < k'+1$

The set of intervals forms a <u>partially ordered set</u> under this ordering relation. It is obviously transitive. No ordering is given between two overlapping intervals; such intervals are said to be <u>unrelated</u>. A subset of intervals $I(v_{j_1}), I(v_{j_2}), \ldots$

 (v_{j_s}) is said to be <u>completely ordered</u> if

$$|\langle v_{j_1} \rangle < |\langle v_{j_2} \rangle < \dots < |\langle v_{j_s} \rangle|$$

We will refer to such a completely ordered subset as a <u>chain</u>. Obviously, values v_1, v_2, \ldots, v_s associated with the intervals in a chain may all be stored in the same storage location.

The problem of finding the minimal number of storage locations is thus the same as that of decomposing a partially ordered set into disjoint subsets, each of which is completely ordered. This is called a <u>chain decomposition</u>. A constructive procedure for doing this is given by one of the authors, joint with Alan Hoffman [1],

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in connection with Dilworth's Theorem [2]. In our special application here to the partially ordered set of intervals, there is available, however, a much easier procedure. This can be found in Ford and Fulkerson [3]. Applied here, it yields:

RULE: Store v_k in the same location as any v_i not needed for any step after k.

It is obvious that the application of the rule provides a valid storage procedure and it is probably equally obvious that the rule yields a minimal number of storage locations. We will, nevertheless, give a formal proof.

Up to step k, let T_{k-1} be the subset of locations used to store the values $v_1, v_2, \ldots, v_{k-1}$. Let L be any location in the set T_{k-1} , and v_i the last value stored in L at the start of step k. Several values may have previously been stored in L, but v_i refers only to the last one stored in L up to step k; let I_i be the storage interval of this v_i .

It is clear that v_k cannot be stored in L if I_L overlaps with $I(v_k)$. If $I(v_k)$ overlaps with every interval I_L for all $L \in T_{k-1}$, then it is necessary to increase the set of storage locations in order to store v_k . In this case, the number of storage locations in T_k has to be one greater than N_{k-1} , the number of locations in T_{k-1} . In general, $N_k = 1 + N_{k-1}$ or $N_k = N_{k-1}$. Let us suppose that on step k, there was a location $L \in T_{k-1}$ such that I(L) does not overlap with $I(v_k)$, but that a location \bar{L} not in T_{k-1} was used instead for storing v_k . Note that on subsequent steps the values stored in L or \bar{L} could be interchanged if on step k, location L were used in place of \bar{L} . This interchange never increases the count of the locations used and the count could even be decreased if \bar{L} is never used and is dropped.

Thus we have shown that there always exists a minimal storage selection that always stores for each k the value v_k in T_{k-1} unless I_L for all $L \in T_{k-1}$

overlaps with $I(v_k)$. We wish to show that any selection with this property is minimal. Let $k = k^*$ be the lowest index k such that $N_k = \text{Max } N_k$; then $k^* = N_k$. Thus every interval I_k for $k^* = N_k$ overlaps with that of $k^* = N_k$. But each such interval begins before $I(v_k)$, hence all overlap with the value $k^* + 1$, the start of interval $I(v_k)$. Thus all N_k intervals of k^* have the value $k^* + 1$ in common, and constitute a set of N_k unrelated intervals in the partially ordered set of intervals.

Note that N happens to be also equal to the number of storage locations selected to carry out the computations. Associated with each location LeT is the subset of values v_i stored in L on steps 1,2,...,n. The intervals $l(v_i)$ of these v_i are completely ordered, hence form a chain.

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Thus for each LeT * , there is associated a mutually exclusive chain, and k* every interval in the original partially ordered set belongs to one of these chains. Thus we can decompose the partially ordered set into N * non-over-k* non-over-k* non-over-k* always belong to different chains, the minimum chain-decomposition must always be greater or equal to the maximum number of unrelated elements. Hence, when N * , the number of chains in some decomposition, happens to be the same k* as the number of elements in some set of unrelated elements, we conclude that this can only occur when the partially ordered set has been decomposed into a minimal number of chains. This completes our proof. The discussion just given is a paraphrase of the usual proof of sufficiency of the following:

<u>DILWORTH'S THEOREM</u>: The maximum number of unrelated elements in a partially ordered set is equal to the number of chains in a minimal decomposition.

APPLICATION IF THE NUMBER OF STEPS IS SMALL: The task of the compiler will be to set up a correspondence between location addresses and symbols used in the program.

If there are n steps and n is <u>reasonably small</u>, then the following procedure will accomplish the minimum storage of the program. Only if the program is to be executed many times would the method to save storage given below be worthwhile

Set aside n locations A_k for recording ℓ_k , the last step for which ν_k is needed for computation. Scan each step ℓ in turn and record ℓ in A_k if ν_k is required on step ℓ to compute ν_ℓ . The final value of ℓ recorded in each A_k is ℓ_k . Note that for any ν_i which is not required on some subsequent step (such as ν_n) the value in A_i is $\ell_i = 0$.

Set up a way of generating the names of up to n-1 addresses which will be called upon as required as a source of additional addresses for storing v_i . The addresses to be assigned for storing v_i will be stored in n locations B_1 , B_2 ,..., B_n as follows: Generate an address and store in B_1 , except store 0 if $\ell_1 = 0$. For each $k = 1, 2, \ldots, n$, store 0 in B_k if $\ell_k = 0$; otherwise, the same address as in B_i where i_0 is the first $i_0 < k$ such that $0 < \ell_{i_0} \le k$. If there exists no such i_0 , then generate a new location address and store it in B_k . Note that 0 in B_k is to be interpreted as not requiring an address for v_k . To prevent the re-use of ℓ_{i_0} (since it is now superceded by ℓ_k), the value of ℓ_{i_0} in A_{i_0} is replaced by ℓ_{i_0} and the process is then iterated. Finally, assign the address in B_i to v_i .

IF THE NUMBER OF STEPS IS LARGE: A simple example will suffice to show a fundamental difficulty of the previous procedure when the number of steps is large or unspecified until execution time. The following routine (assuming no mistakes) can be used to (inefficiently) rearrange m numbers in ascending order:

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We will call this a generic algorithm because m is not specified until execution time. Here we wish to make a <u>prior</u> decision of what values are to be stored in the same location to be used whatever be the eventual value of m. This particular routing computes $2m^2 + m$ different values. For m = 1,000, say, it would not be practical to apply the method of the previous section. A little study shows that all the A_{ij} may be stored in a single location and all the vectors $\{x_{i,1}, x_{i,2}, \ldots, x_{i,m}\}$ in the same m locations as $\{x_{i-1,1}, \ldots, x_{i-1,m}\}$. Hence only m+1 memory locations are required by this routine to sort m numbers.

This illustrates the more important problem which we are working on, namely that of analyzing the structure of generic routines (i.e., those with unspecified parameters) to determine the minimal assignment to storage prior to specification.

REFERENCES

- Dantzig, G.B. and A.J. Hoffman, "Dilworth's Theorem on Partially Ordered Sets", <u>Linear Inequalities and Related Systems</u>, Annals of Mathematics Study 38, Princeton University Press, pp. 207-214 (1956).
- [2] Dilworth, R.P., "A Decomposition Theorem for Partially Ordered Sets," Annals of Math 5), pp. 161-166 (1950).
- [3] Ford, L.R., Jr., and D. R. Fulkerson, <u>Flows in Networks</u>, Princeton University Press (1962).
- [4] Fulkerson, D.R., "Note on Dilworth's Decomposition Theorem for Partially Ordered Sets," Proc. Am. Math. Soc. 7, pp. 701-702 (1956).

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